



SHEET 1 OF 1

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION

(PTO-1449)

ATTY. DOCKET NO.
61352-057SERIAL NO.
10701,082APPLICANT
Michihito UEDA, et al.FILING DATE
November 05, 2003GROUP
To be assigned

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US			
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FOREIGN PATENT DOCUMENTS

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
/TM/		MORIE ET AL., "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals", IEICE Transactions on Electronics, Vol. E84-C, No. 12, December 2001, pp. 1723-1729

EXAMINER

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